

FIG. 1

10039765 - 140701

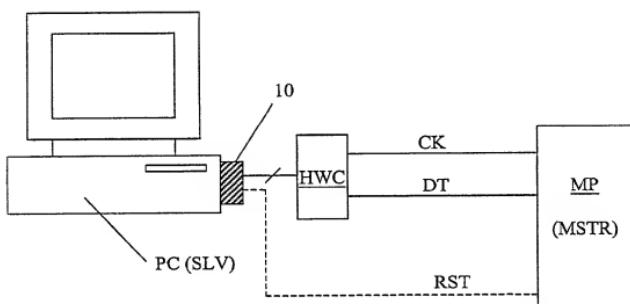
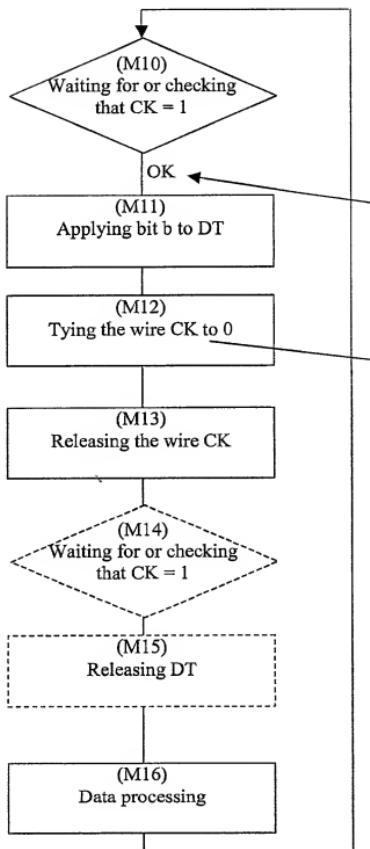


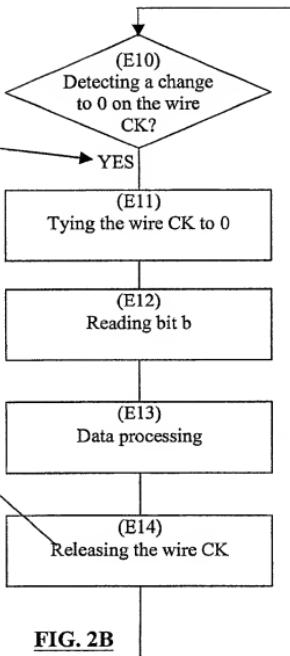
FIG. 7

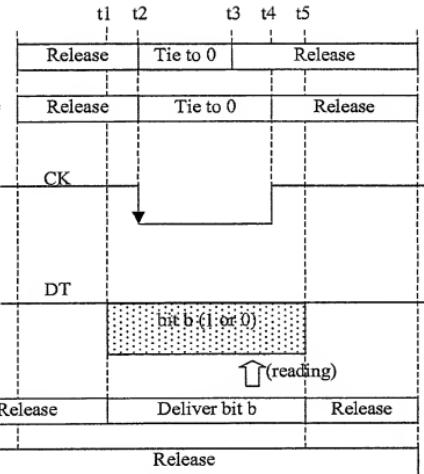
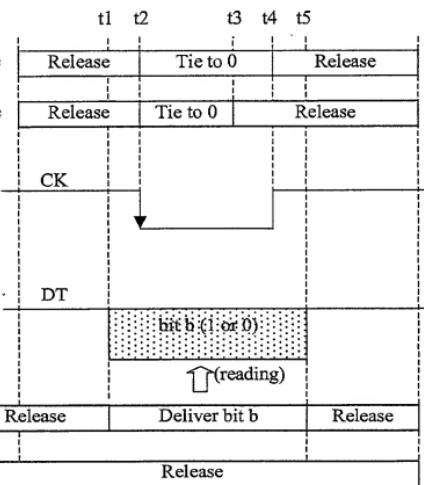
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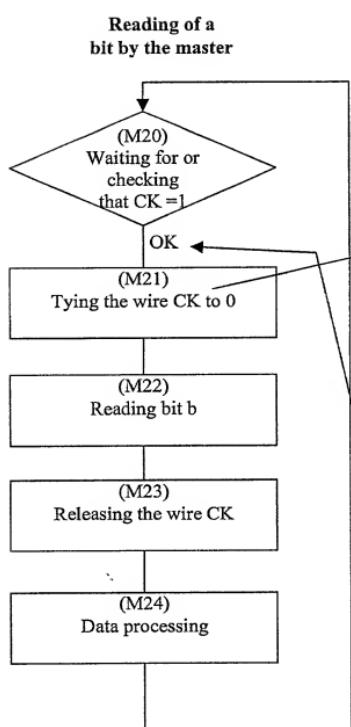
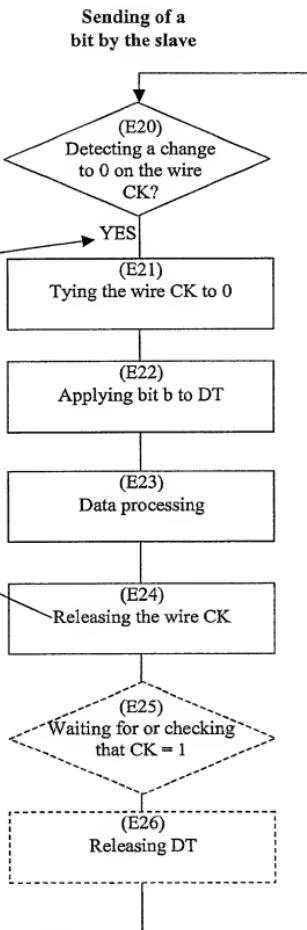
Sending of a bit by the master

**FIG. 2A**

Reading of a bit by the slave

**FIG. 2B**

**FIG. 3A****FIG. 3B**

**FIG. 4A****FIG. 4B**

	t2	t3	t4	t5
Actions by the master on the wire CK	Release	Tie to 0		Release
Actions by the slave on the wire CK	Release	Tie to 0		Release
	CK			
		↓		
	DT			
			bit b (1 or 0)	
				↑ (reading)
Actions by the master on the wire DT				Release
Actions by the slave on the wire DT	Release		Deliver bit b	Release

FIG. 5A

	t2	t3	t4	t5
Actions by the master on the wire CK	Release	Tie to 0		Release
Actions by the slave on the wire CK	Release	Tie to 0		Release
	CK			
		↓		
	DT			
			bit b (1 or 0)	
				↑ (reading)
Actions by the master on the wire DT				Release
Actions by the slave on the wire DT	Release		Deliver bit b	Release

FIG. 5B

Fig. 6

